



IC DIGITAL BACKEND ENGINEER F/M

NanoXplore is a pioneer in design and development of space-grade FPGA and e-FPGA located in Paris and Montpellier. As a rapidly growing company, NanoXplore is looking to continue building its software and hardware teams.

We are currently hiring a digital back-end engineer who will join our hardware R&D team located in Paris. This person will contribute to the physical implementation and validation of NanoXplore's digital IPs.

POSTE ET MISSIONS

In NanoXplore FPGA design process, custom and foundry standard-cell based digital design flows are combined for highest area and performance efficiency. You will be in charge of RTL-to-GDS flow for physical implementation of foundry standard-cell based design at technology nodes 28 nm and below. Your main missions will be:

- Improvement of existing design flow and its maintenance.
- Participation to specification phase for performance and power optimization of digital IPs.
- Physical implementation from floorplanning to timing closure (RTL2GDS).

REQUIRED SKILLS

- Relevant experience in IC physical implementation with proven tape-out record in advanced technology nodes.
- Hands-on experience in all aspects of the ASIC development process with proficiency in back-end tools and methodologies (synthesis, P&R, timing analysis).

DESIRED SKILLS:

- Physical design knowledge of device physics and in particular aspects which impact timing (cross talk, noise, OCV, etc.)
- Design experience with low-power and clock tree synthesis.
- Scripting experience (Makefile, TCL and Python).

COMPANY

Join our highly motivated team and be part of our success story "à la française," in a company with a start-up mentality. We have always believed that our limits are drawn by your motivation. Always attentive to the wellbeing of its employees, NanoXplore allows you to grow and to develop your skills in a quick-paced, reactive working environment. Have any questions about what do we do? The Company? Daily life? Please contact us at careers@nanoxplore.com.

SUMMARY

- Contract: CDI – full-time.
- Location : SEVRES - Île-de-France.
- Experience: minimum 5 years of relevant experience.
- Salary: based on your profile.